



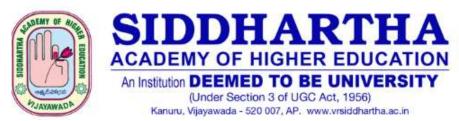
Kanuru, Vijayawada - 520 007, AP. www.vrsiddhartha.ac.in



DEPARTMENT FO ELECTRICAL &ELECTRONICS ENGINEERING

Name of the Event	Circuit Debugging
Objective	The objective of this event is to test participant's ability to analyse and debug faulty circuits, enhancing their problem-solving skills in networks.
Eligibility	Open to all students with an interest in technology. Those who are register for SUPRAGNA.
Team Size	Individual
Rules & Regulations	 Description: Participants are provided with faulty circuits and they must identify, fix the errors within the given time limit and also they need to solve the given circuit. 1.Game Rounds: There will be three rounds. Round 1: Participant's are provided with a question paper which they need to solve and identify and fix the errors. There will be three levels of questions such as hard, medium, easy levels. The hard level question carries 3 marks, medium level question carries 1 mark. Duration: 20minutes ➢ The top-scoring participant's will qualify for the second round. Round 2: In this interactive activity, participants are shown an image of a specific component and are then asked questions related to that component. The objective is to assess their understanding and knowledge about the component's features, functions, and applications.

	Round 3(Final Round): The qualified
	participant's will be provide with a circuit
	diagram, They need to build the circuit with the
	required components.
	2. Components: Components will be
	provided.
	4. Scoring System: Points will be awarded
	based on accuracy and correct answers in the
	given time. No extra time will be allotted.
	5. No external reference materials, mobile
	phones, or internet access will be allowed
	during the event.
	6. If anyone fails to debug within the given
	time, they will be disqualified.
	7. Participants must bring their college Id cards
	for verification.
	8. All decisions will be final by the Judges and
	Co-Ordinators.
	9. All participants must report 15 minutes
	before the event starts.
Faculty Co-Ordinators & Contact Details	Dr. A. Veera Reddy
	Mr. P. Venkatesh
Student Co-Ordinators & Contact Details	M. Gayatri(9392158031)
	T. Vasundhara Devi(9505470136)



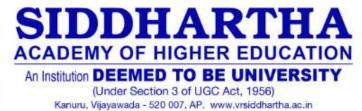


Department of Electrical & Electronics Engineering

Name of the Event	IoT Marathon
Objective	To challenge participants in designing and developing IoT-based solutions within a limited time. The event will test their knowledge of microcontrollers, sensors, and real-time data processing
Eligibility	Open to all students with an interest in technology. Those who are registered for SUPRAGNA-2025
Team Size	Max of 3 members in a group
Rules & Regulations	 Round 1: IoT Quiz (Elimination Round) – A multiple-choice quiz on IoT fundamentals, microcontrollers, and communication protocols. Top teams qualify for the next round. Round 2: Circuit Debugging & Sensor Integration – Teams must debug a faulty IoT circuit or code and make it functional within a given time. Successful teams move to the final round. Final Round: IoT Prototype Challenge – Teams will be given a problem statement (e.g., Smart Irrigation, Fire Detection) and
	must design a working model using the provided components. Solutions will be

	judged on functionality, innovation, and
	presentation.
	Rules & Regulations
	1. Teams must complete all tasks within the given time.
	2. Participants can use only the provided hardware and software.
	3. The quiz round will be an elimination round.
	4. Debugging and prototype challenges will be judged based on accuracy, innovation, and functionality.
	5. Any form of malpractice will lead to disqualification.
	6. Judges' decisions will be final.
Faculty Co-ordinators	Dr. N. Vamsi Krishna
& Contact Details	Mr. V. Ravindranadh Chowdary
Student Co-ordinators	Md. Rayyan - 94941 13166
& Contact Details	Sk. Suhana – 70361 96547

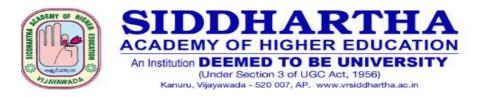






DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Name of the Event	Treasure Hunt
Objective	A fun and challenging event where teams solve clues and complete tasks to find hidden treasures across the college premises.
Eligibility	Open to all students those who registered for SUPRAGNA-2025.
Team Size	Maximum 3 members in a group.
Rules & Regulations	 Teams must follow the sequence of clues to find the final treasure. No use of mobile phones or external help is allowed during the event. Teams must complete each challenge before proceeding to the next clue. Teams must maintain fair play, tampering with clues or misguiding others will lead to disqualification. The team that finds the treasure first will be declared the winner. The participants must report 15 min before the event starts.
Faculty Co-ordinators &	Mr. R. Madhusudhan Rao &
Contact Details	Dr.T. Naveen kumar
Student Co-ordinators & Contact Details	S. Manideep [9182483874] & S. Sai Rekha [8106975799]





DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Name of the Event	Talent Show
Objective	To provide a platform for students to
	showcase their unique talents and creativity
Eligibility	open to all students who have been
	registered for SUPRAGNA-2025
Team Size	Max 4 or Solo
No. of prizes	Two
	1. Singing, dancing, acting, musical
	instruments, mimicry, magic, stand-
	up comedy, and other creative
	talents.
	2. Each performance should not exceed 5
	minutes.
	3. Participants must bring their own props,
	musical instruments, and other required
Rules & Regulations	materials. The organizing team will
	provide only basic audio setup.
	4. Participants will be judged based on
	creativity, originality, stage presence,
	audience engagement, and overall
	impact.
	5. Performances involving dangerous
	stunts, fire, or offensive content will not

	 be allowed. All acts must be approved by the event coordinators before the competition. 6. Any act containing offensive language, inappropriate gestures, or disrespectful content will be disqualified. 7. Participants must report 30 minutes before their scheduled performance
Faculty Co-ordinators &	Mr. V. Hari Vamsi
Contact Details	Mrs. B. Swarupa Rani
	N.Rushi
Student Co-ordinators	Contact: 6302733616
& Contact Details	CH.Venkata Sai Sujith
	Contact: 9640458372